In the Abstract, please amend as follows:

~~The~~ An integrated circuit ~~comprises:~~ including a shared memory connected to a bus~~,~~; an audio/multiplex/de-multiplex processor accessing the shared memory via the bus~~,~~; a video processor performing heavy processes accessing the shared memory via the bus~~,~~; and a local memory accessed by the video processor without passing through the bus. ~~Avoiding~~ The integrated circuit avoids a latency time caused by access contention, such that a probability that the integrated circuit can complete processes to be done in real time is increased ~~increases~~. Image data is displayed on the display device smoothly without deterioration of quality of display.